

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Hiroshi HASHIMOTO et al.**

Serial Number: **Not Yet Assigned**

Filed: **January 14, 2004**

Customer No.: 38834

For: **METHOD OF PRODUCING SEMICONDUCTOR DEVICE**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

January 14, 2004

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge
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Respectfully submitted,
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Enclosures: PTO-1449; Reference (3)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 042017	Serial No. New Appln.
	Applicant(s): Hiroshi HASHIMOTO et al.	
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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
—	AA					
—	AB					
—	AC					
—	AD					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
—	AE 9-129721	05/16/97	Japan	Abstract
—	AF 2001-203285	07/27/01	Japan	Abstract. Discussed in the specification.
—	AG 2002-349164	12/04/02	Japan	Abstract. Discussed in the specification.
—	AH			
—	AI			

OTHER DOCUMENTS

—	AJ	
—	AK	
Examiner	Date Considered	